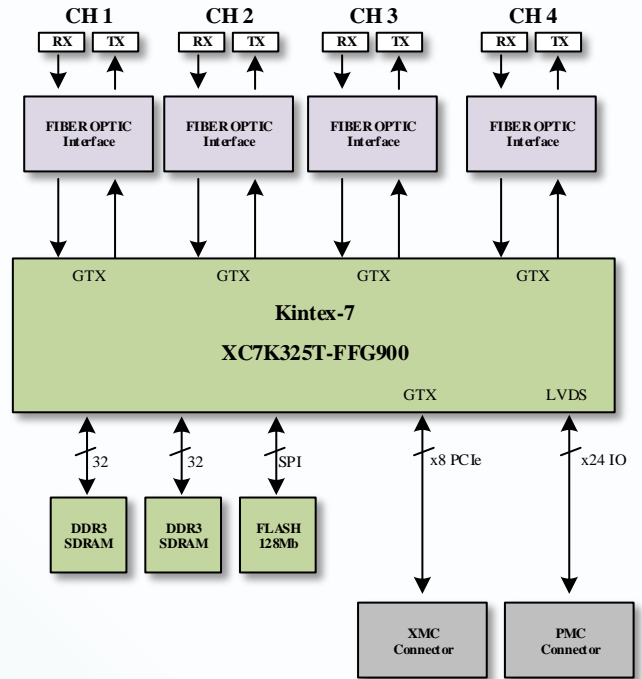


Features

- Complete serial FPDP solution
- Fully compliant with VITA 17.1 specification
- Fiber optic or copper serial interfaces
- Up to 1 GB of DDR3 SDRAM
- 8x PCI Express interfaces
- Support 2.5G and 3.125Gbaud Link rates



Specifications

| Item | Description | Remark |
|-------------------|--|---|
| FPGA | Xilinx Kintex-7 FPGA | Kintex-7 Series |
| Flash Memory | 128Mb Flash In-System Programmable Configuration SPI ROMs | N25Q128 |
| Memory | 4x DDR3 SDRAM 32 Meg x 16 x 8 Banks | (Option) |
| Fiber Optic | 4x Fiber Optic Transceiver up to 4.25Gb/s Duplex data links | RJ-4G-SX-DPLX-LC-R-A-x |
| Software Drivers | Various software drivers can be offered including Linux as standard; others are costed options. Sample programs for driver usage (C/C++ source code) | |
| Environment | Operating | -45°C to +85 °C |
| | Storage | -50°C to +100 °C |
| | Humidity | 0% - 95% non-condensed |
| | Vibration | 10 g (peak) 5 Hz to 2 kHz 0,1 g ² /Hz at 15 Hz to 2 kHz |
| Form Factor | XMC, PMC | |
| Power Requirement | DC+5Volt 0.5A(MAX) DC+3.3Volt 2A(MAX) | |
| Dimensions | 160 mm x 74mm x 15mm | |
| Weight | 112g | |